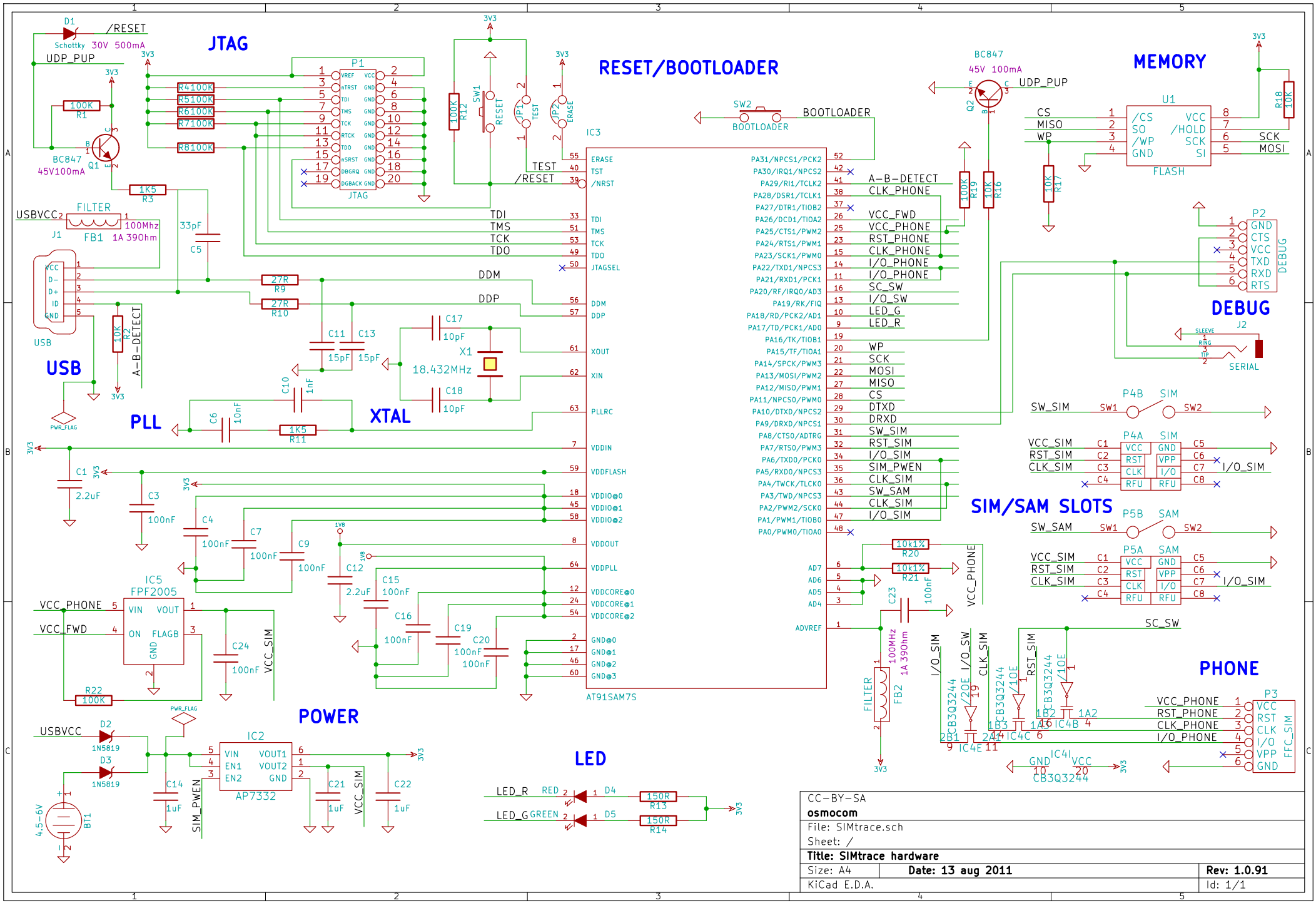
JTAG
RESET/BOOTLOADER
MEMORY
USB
PLL
XTAL
DEBUG
SIM/SAM SLOTS
POWER
PHONE
LED
AT91SAM7S
IC5 FPF2005
IC2 AP7332
U1 FLASH
P1 JTAG
P2 DEBUG
P3 FFC_SIM
P4A SIM
P4B SIM
P5A SAM
P5B SAM
J1 USB
J2 SERIAL
BT1 4.5-6V
D1 Schottky 30V 500mA
BC847 45V100mA
FB1 1A 390Ohm
FB2 1A 390Ohm
X1 18.432MHz
SW1 RESET
SW2 BOOTLOADER
JP1 TEST
JP2 ERASE
CC-BY-SA
osmocom
File: SIMtrace.sch
Sheet: /
Title: SIMtrace hardware
Size: A4
Date: 13 aug 2011
Rev: 1.0.91
KiCad E.D.A.
Id: 1/1