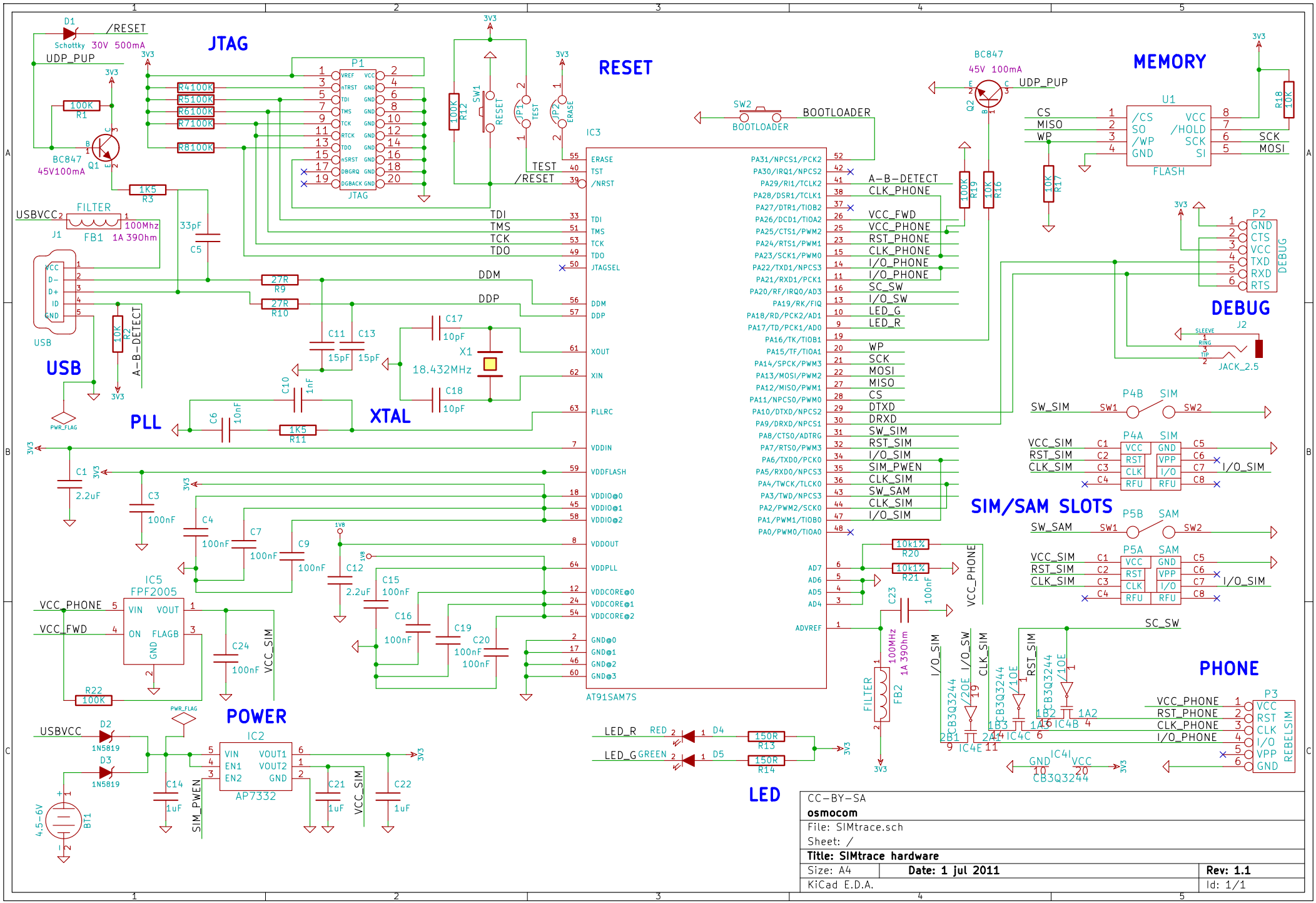
JTAG
RESET
MEMORY
USB
PLL
XTAL
DEBUG
SIM/SAM SLOTS
POWER
PHONE
LED
BOOTLOADER
/RESET
UDP_PUP
Schottky 30V 500mA
BC847
45V100mA
P1
JTAG
TDI
TMS
TCK
TDO
TEST
ERASE
JTAGSEL
FILTER
FB1 1A 390hm
100Mhz
USBVCC
A-B-DETECT
DDM
DDP
18.432MHz
IC3
AT91SAM7S
IC5
FPF2005
IC2
AP7332
FLASH
SIM
SAM
DEBUG
REBELSIM
JACK_2.5
SW_SIM
SW_SAM
VCC_PHONE
VCC_FWD
VCC_SIM
SIM_PWEN
I/O_SIM
SC_SW
LED_R
LED_G
CC-BY-SA
osmocom
File: SIMtrace.sch
Sheet: /
Title: SIMtrace hardware
Size: A4
Date: 1 jul 2011
Rev: 1.1
KiCad E.D.A.
Id: 1/1